



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Simon Dodd et al.

Appl. No : 10/055,161

Filed: October 26, 2001

For: "DEVICES AND METHODS  
FOR INTEGRATED CIRCUIT  
MANUFACTURING"

) Examiner: Clark, Jasmine Jhihan B.

) Art Unit: 2815

) Our Ref: B-4742 940653-8

) Date: September 20, 2002

) Re: **Response**

PATENT

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**RESPONSE**

Hon. Commissioner for Patents  
Washington, D. C. 20231

Sir:

This paper is filed in Response to the Official Action dated July 22, 2002. Filed with this response is a petition for a one-month extension of time with the appropriate fee. **All amendments and remarks herein are made without prejudice.**

**IN THE CLAIMS**

Please cancel claims 1-13 and 21-22 without prejudice. Please add new claims 23-27. The REMARKS section of this response discusses new claims 23-27.